

## The Hardware Rendering Pipeline



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## Rendering Images

Geometric model



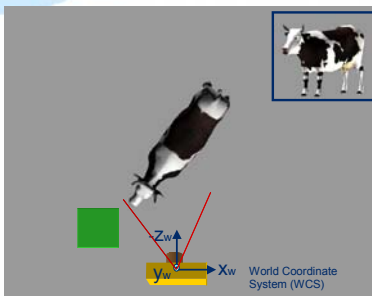
rendering  
algorithm

Picture



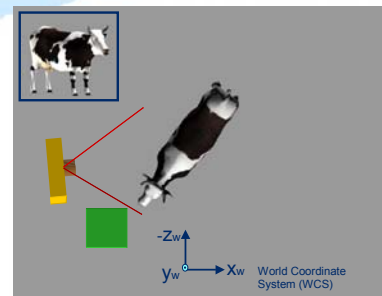
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## The Visualization Problem



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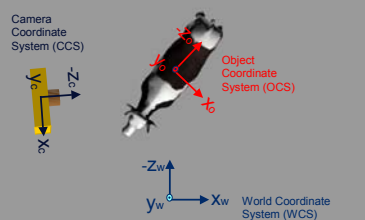
## The Visualization Problem



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## The Visualization Problem

Need to change coordinate systems!



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## Rendering Pipeline (Overview)

Z-buffer and Gouraud shading



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## Rendering Pipeline (Overview)



- Map from OCS to CCS
- Clipping in 3D
- Lighting



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## Rendering Pipeline (Overview)



- Map from OCS to CCS
- Clipping in 3D
- Lighting
- Map from CCS to Screen CS
- Persp. division



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## Rendering Pipeline (Overview)



- Map from OCS to CCS
- Clipping in 3D
- Lighting
- Map from CCS to Screen CS
- Persp. division
- Color
- Visibility



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## Rendering Pipeline (Overview)

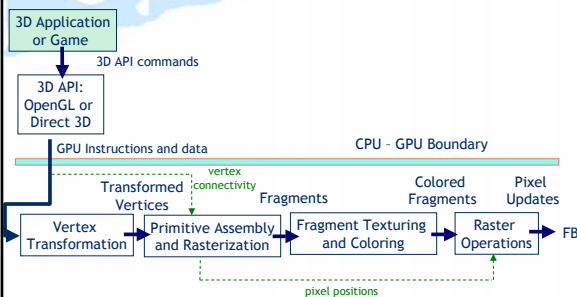


- Map from OCS to CCS
- Clipping in 3D
- Lighting
- Map from CCS to Screen CS
- Persp. division
- Color
- Visibility
- FB display



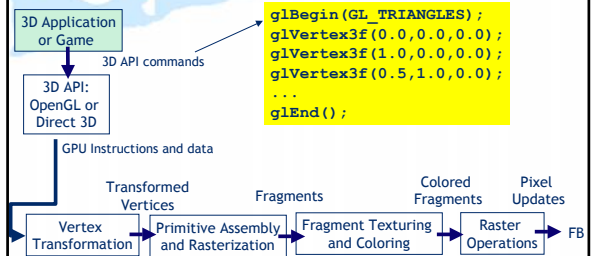
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## Graphics Hardware **Fixed** Pipeline

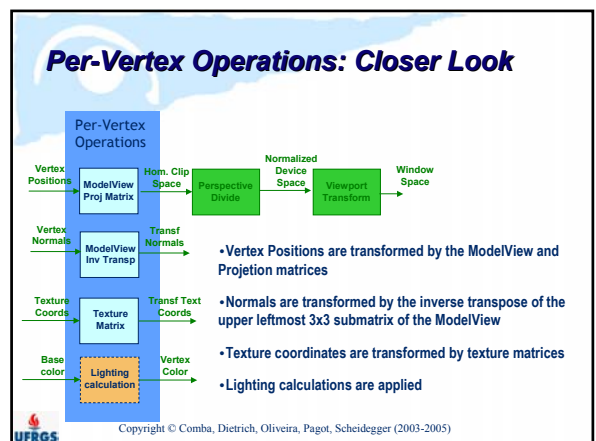
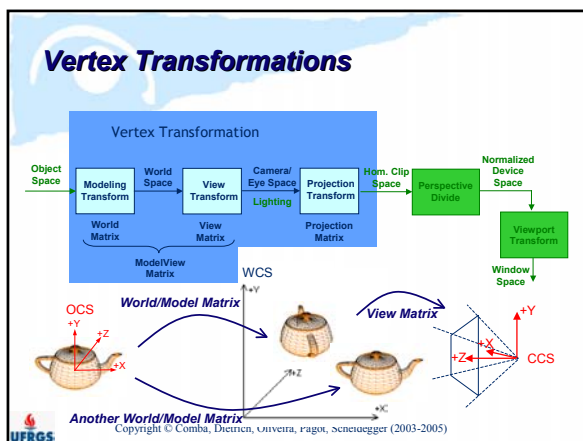
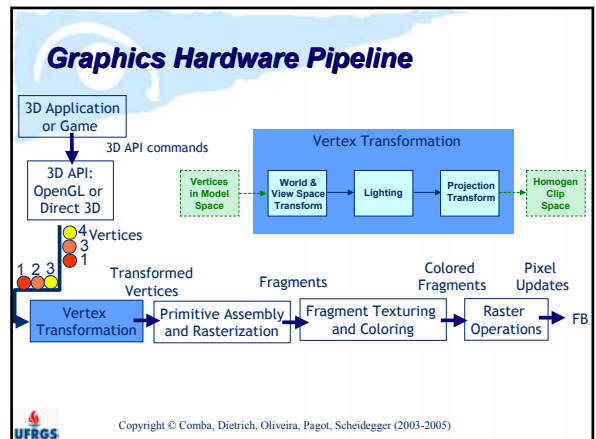
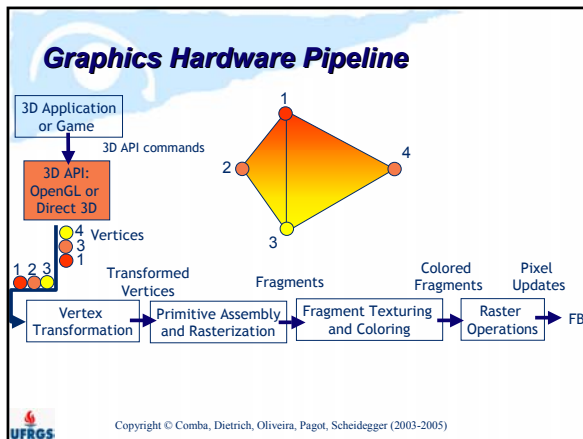
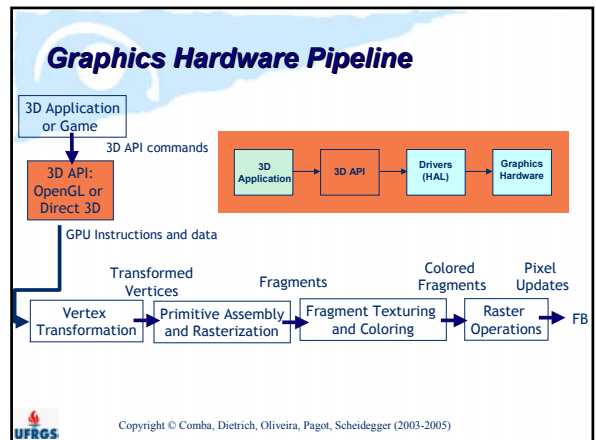
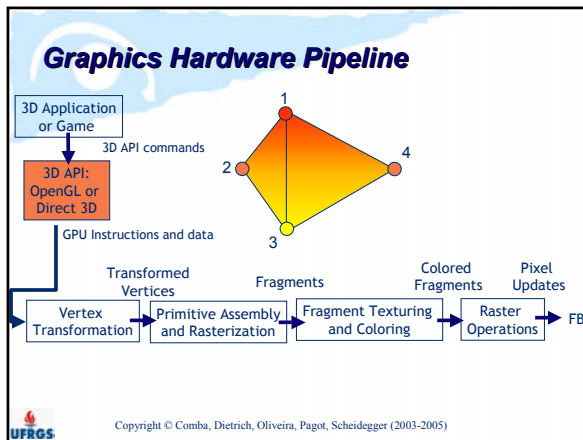


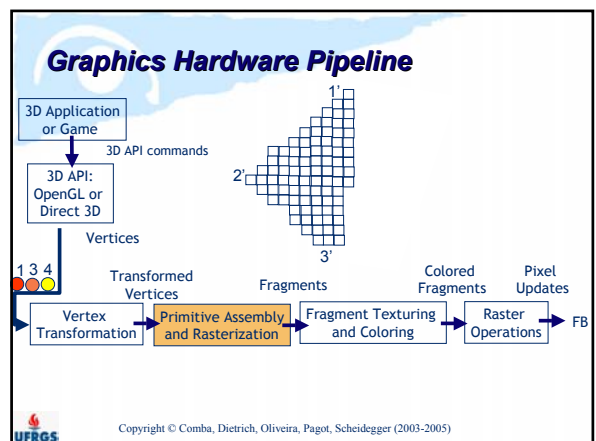
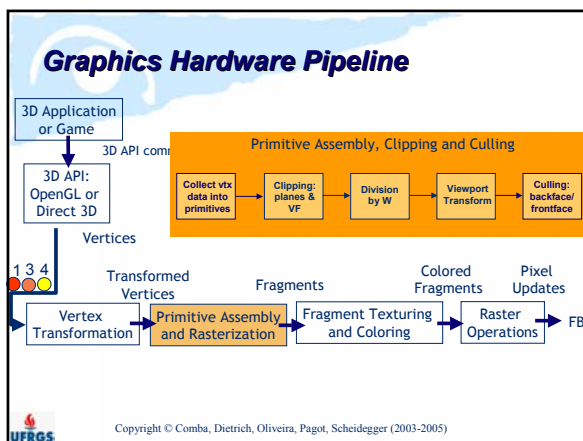
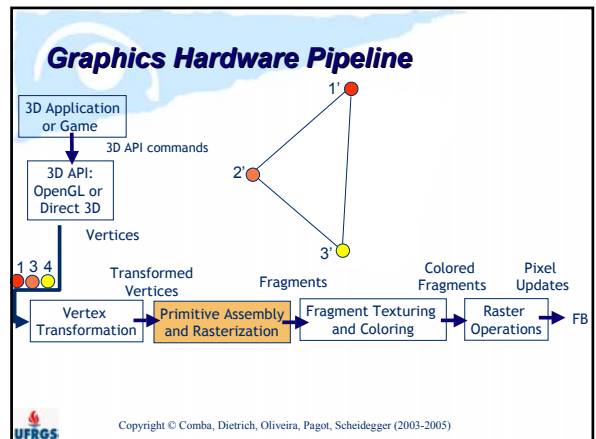
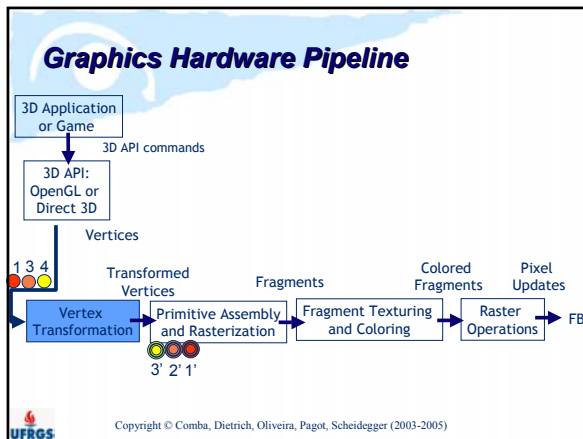
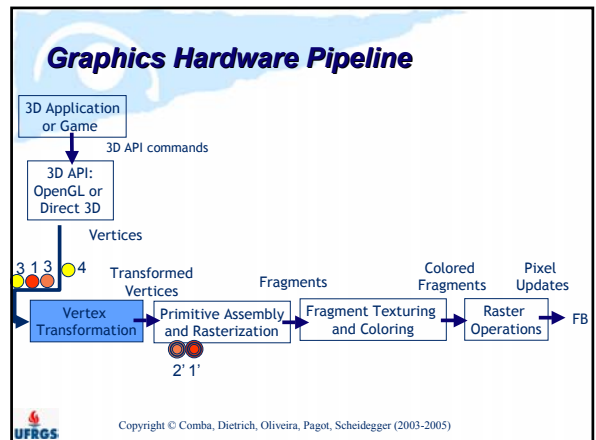
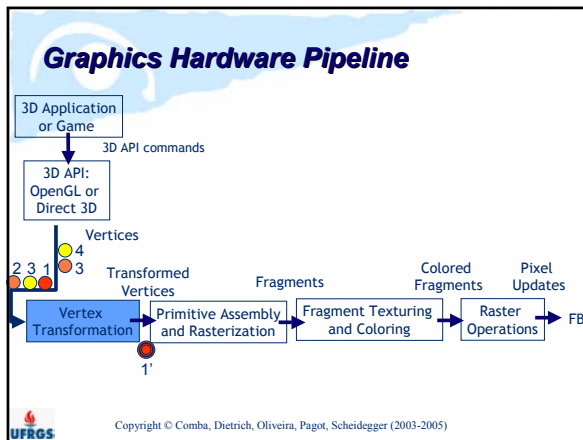
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## Graphics Hardware Pipeline



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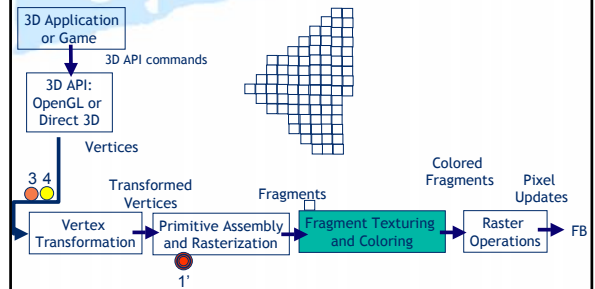
## Rasterization

- Primitives are decomposed into smaller units (*fragments*) corresponding to pixels in the frame buffer
- A fragment has several attributes
  - Window coordinates, depth, color, texture coordinates, ...
- The values of each attribute are determined by interpolation from the values specified at the vertices
- The shading model (SMOOTH / FLAT) is important at this stage
- Antialiasing is also performed at this stage



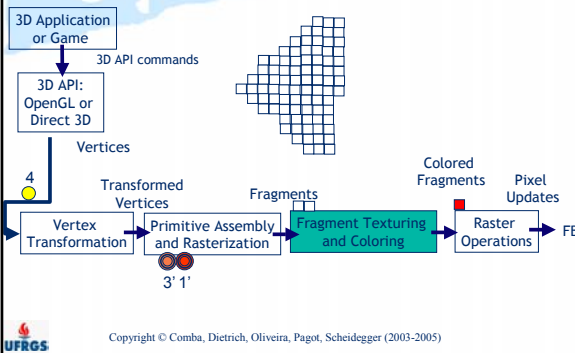
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## Graphics Hardware Pipeline



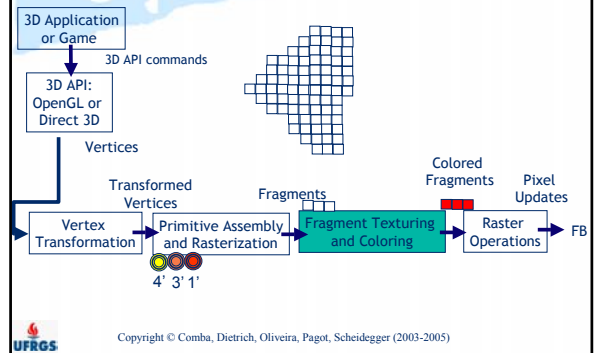
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## Graphics Hardware Pipeline



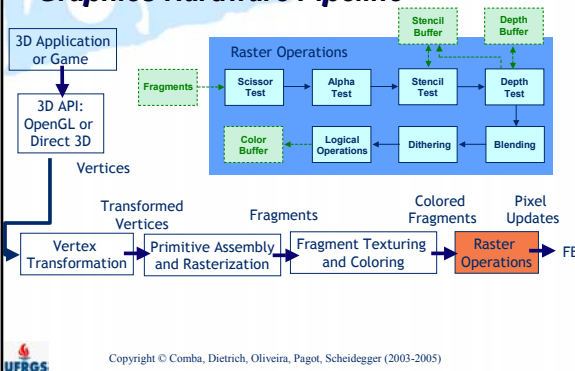
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## Graphics Hardware Pipeline



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## Graphics Hardware Pipeline



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## Raster Operations

- **Scissor Test**
  - Check if the fragment is inside a rectangular portion of the window
  - A version of the stencil test (easy to design very fast hardware for)
- **Alpha Test**
  - In RGBA mode, accepts or rejects a fragment based on its alpha value
  - Requires a reference value and a comparison function (NEVER, ALWAYS,  $<$ ,  $\leq$ ,  $>$ ,  $\geq$ ,  $=$ ,  $\neq$ )
  - **Application:** Transparency algorithms
- **Stencil Test**
  - Requires a stencil buffer (sb), a comparison function and a mask
  - Compares a reference value with the value stored at a pixel in the sb
  - The value in the sb can be modified depending on the result of the test
  - Actions for when: the test fails, the z test succeeds, the z test fails
  - **Application:** Mask out an irregularly shaped region of the screen



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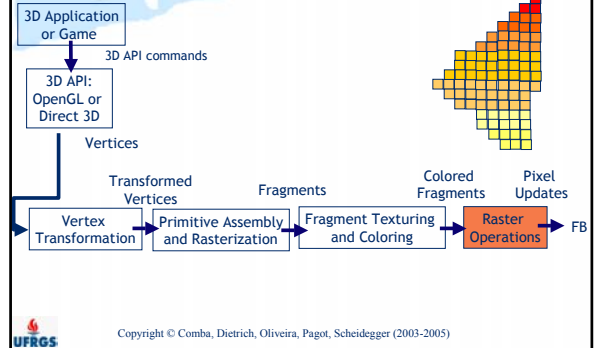
## Raster Operations

- **Depth Test**
  - Check if the fragment is closer to the camera than what is in the depth buffer for the pixel
- **Blending**
  - Combines the incoming R, G, B, A values with what is in the color buffer
  - The result depends on the incoming A and the A value in the color buffer
  - **Application:** Transparency algorithms
- **Dithering**
  - Improves color resolution on systems with small number of color bitplanes
- **Logical Operations**
  - Allows logical operations involving the incoming fragment values (s) and the ones in the color buffer (d)
  - Ex.: AND ( $s \wedge d$ ), OR ( $s \vee d$ ), NAND ( $\neg(s \wedge d)$ ), NOR, XOR, COPY (s), ...



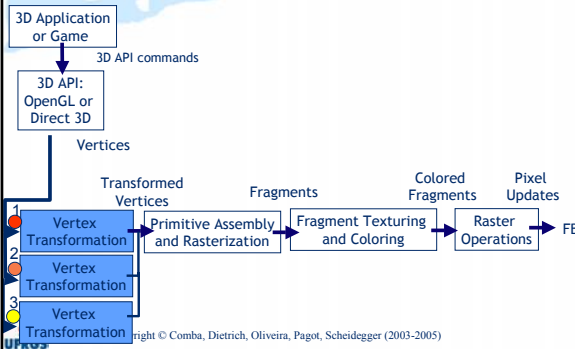
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## Graphics Hardware Pipeline



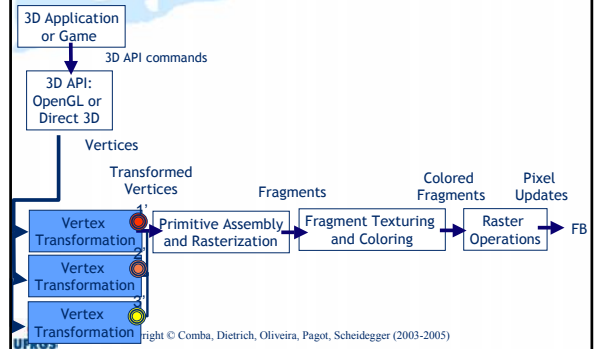
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## Graphics Hardware Pipeline: Parallelization



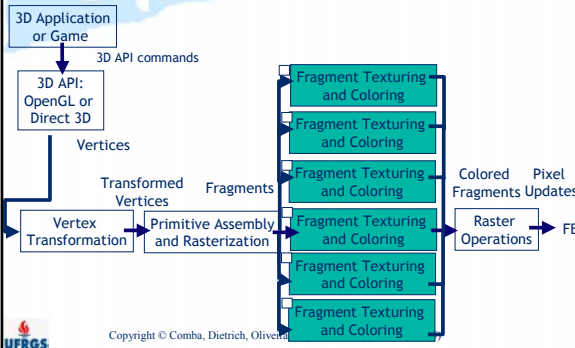
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## Graphics Hardware Pipeline: Parallelization



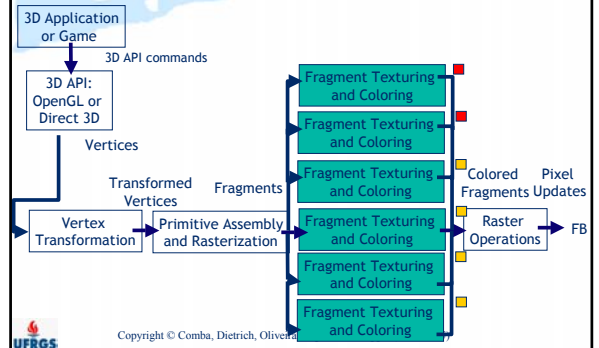
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## Graphics Hardware Pipeline: Parallelization

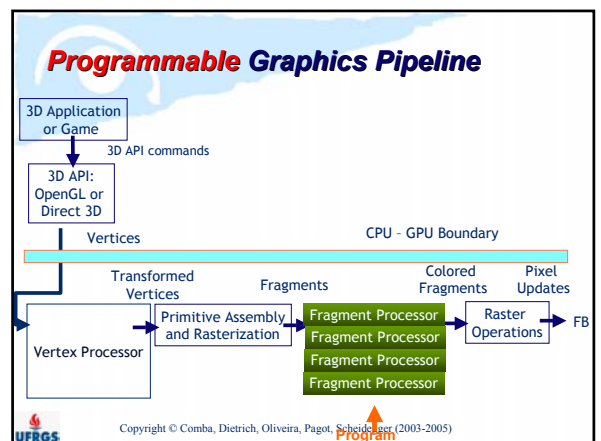
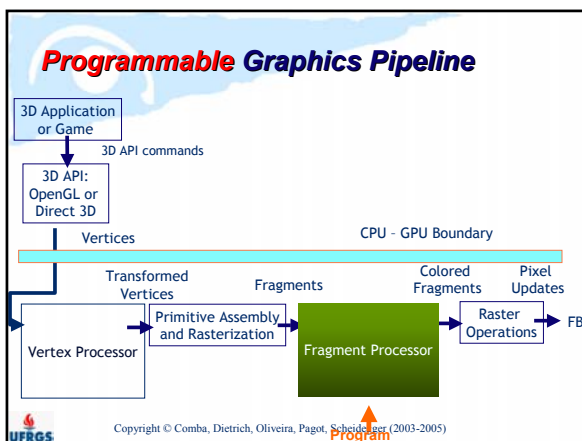
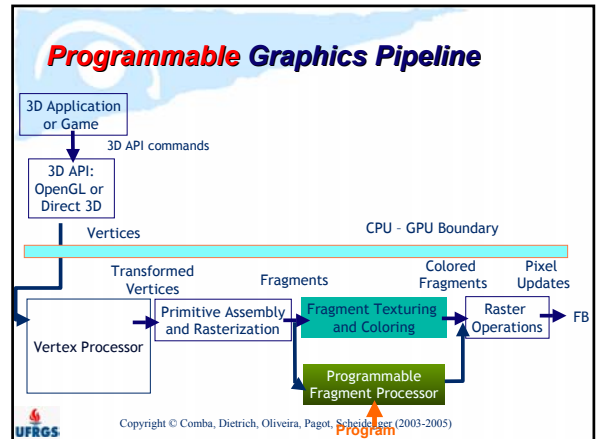
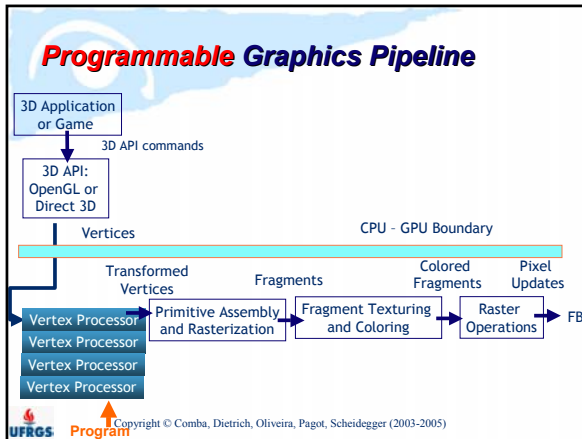
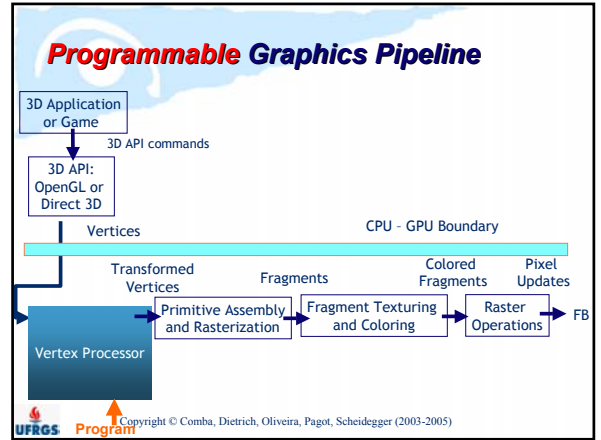
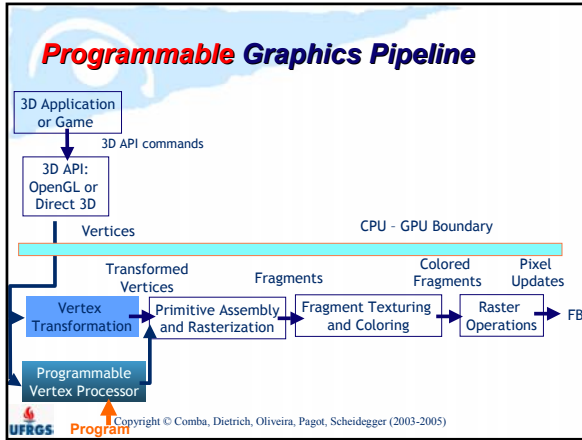


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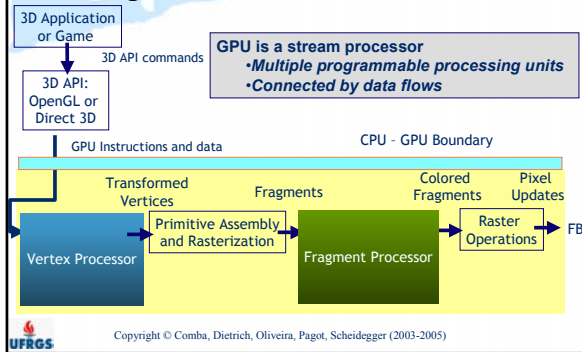
## Graphics Hardware Pipeline: Parallelization



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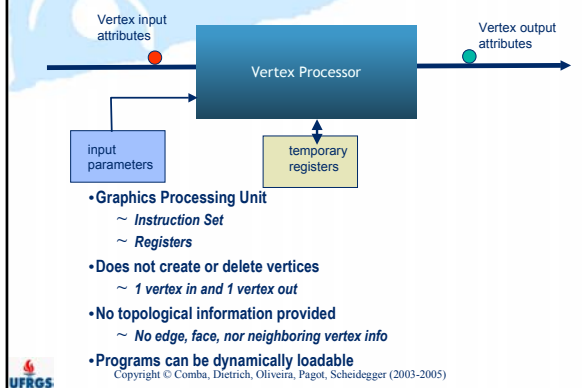


## Graphics Hardware Pipeline: The Big Picture

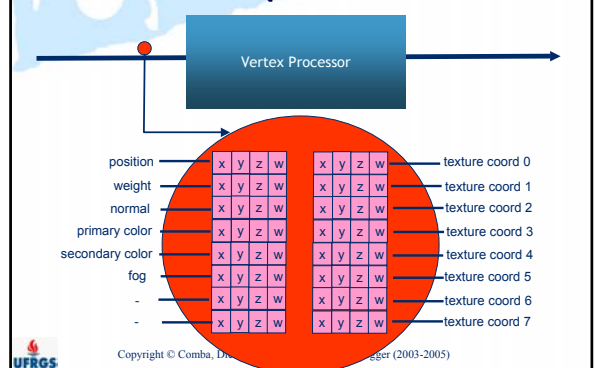


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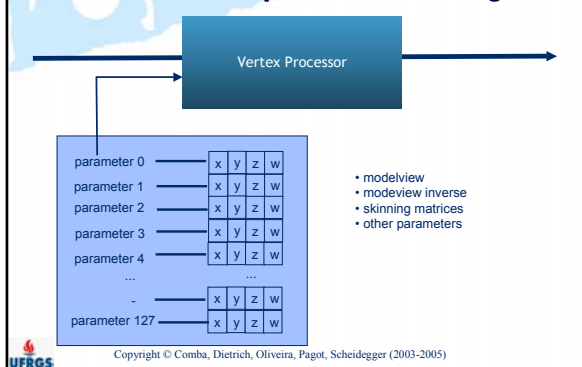
### Vertex Processor



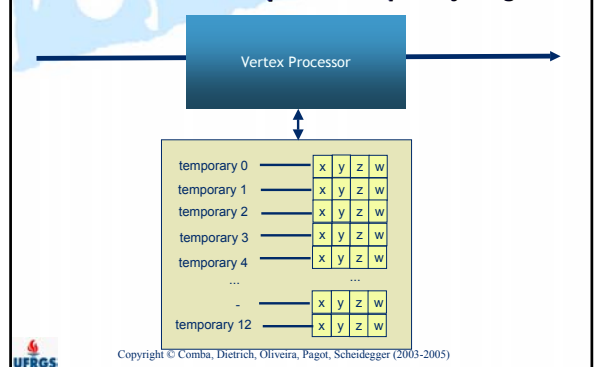
### Vertex Processor Inputs: Vertex Attributes



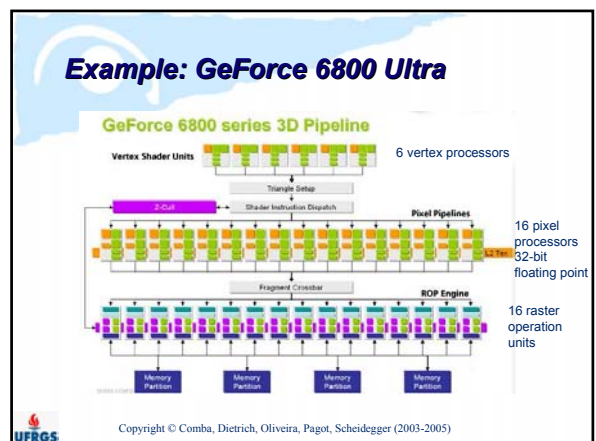
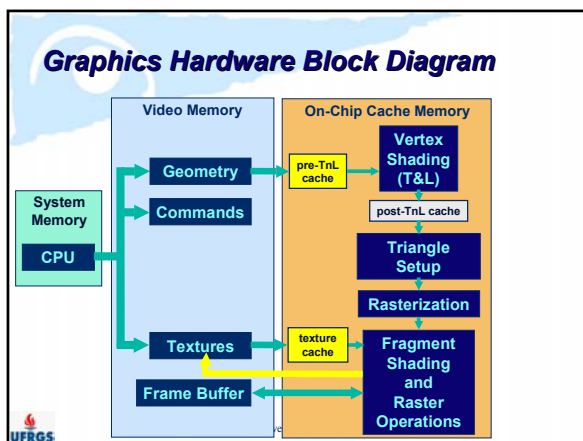
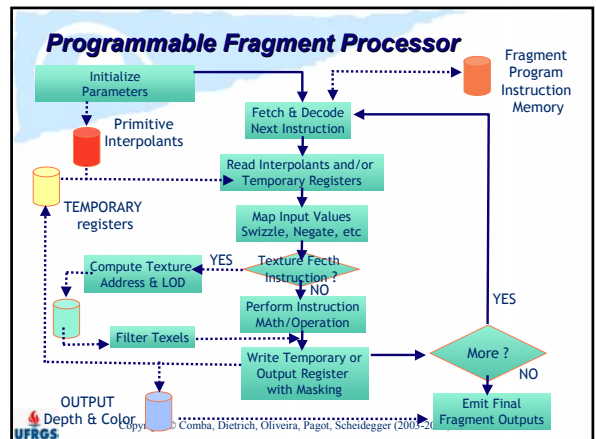
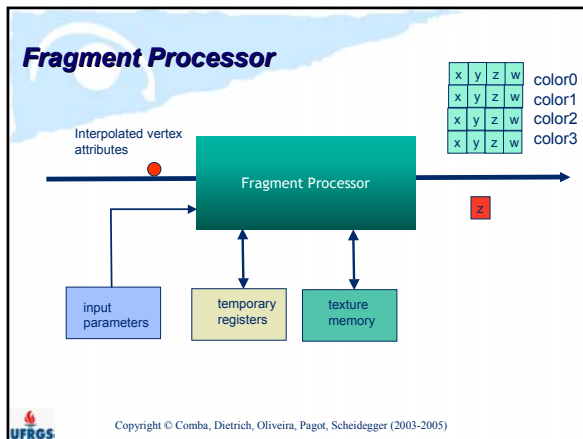
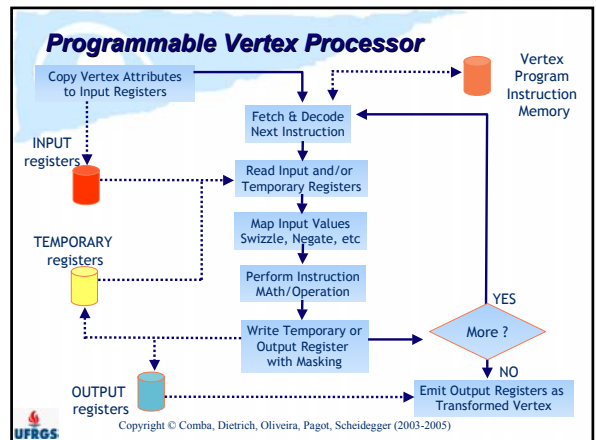
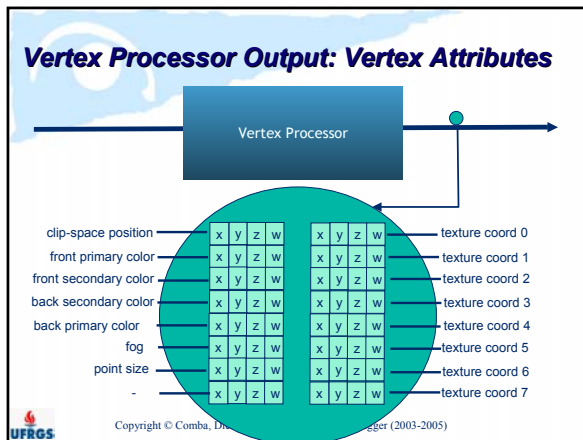
### Vertex Processor Inputs: Constant Registers



### Vertex Processor Inputs: Temporary Registers

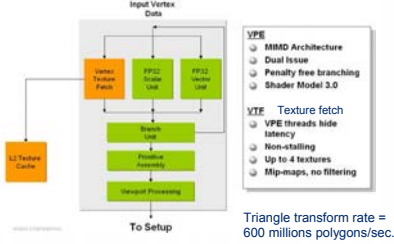






## Example: GeForce 6800 Ultra

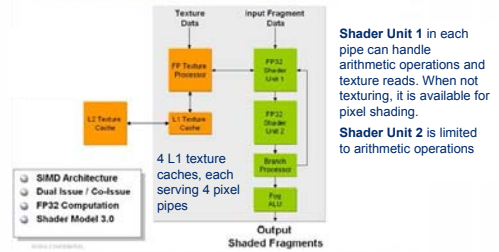
### Detail of a single vertex shader pipeline



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## Example: GeForce 6800 Ultra

### Detail of a single pixel shader pipeline



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## Example: GeForce 6800 Ultra

### new features of the pixel shader engine

- Full Support for shader model 3.0
- 65,535 length pixel shader programs
- Dynamic Flow control - Loops & Branching, Call & Return, Subroutines
- Highest precision pixel shading - Native/optimized FP32 processing
- Flexible data type support - FP32, FP16 operand & texture formats
- Multiple Render Target Support



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## Shader Model 3.0

Vertex shader feature	Shader 2.0	Shader 3.0	Description
Shader length	256 Instructions	65535 instructions	More instructions allow more detailed character lighting and animation
Dynamic branching	No	Yes	Saves performance by skipping animation and calculations on irrelevant vertices
Vertex texture	No	Any number of lookups from up to 4 textures	Allows displacement mapping, particle effects
Instancing support	No	Required	Allows many varied objects to be drawn with only a single command



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