

Reduzindo o Consumo Energético por Controle Dinâmico de Paralelismo em Roubo de Tarefas

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Outline

1 Energy

2 Technique

3 Experiments

4 Conclusion

5 References

What is the relevance of energy in HPC?

- The energy cost of supercomputers is going up [1]
- High power is a performance-limiting factor [2]
 - Heat
 - \$
- Energy is one of the major challenges in the path towards exascale computing [3]

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Energy

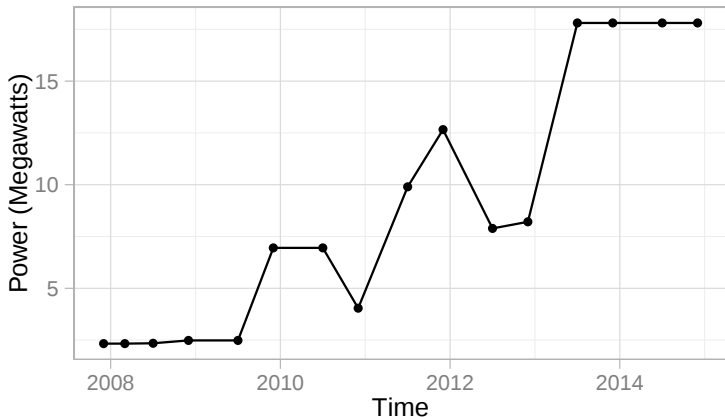
Technique

Experiments

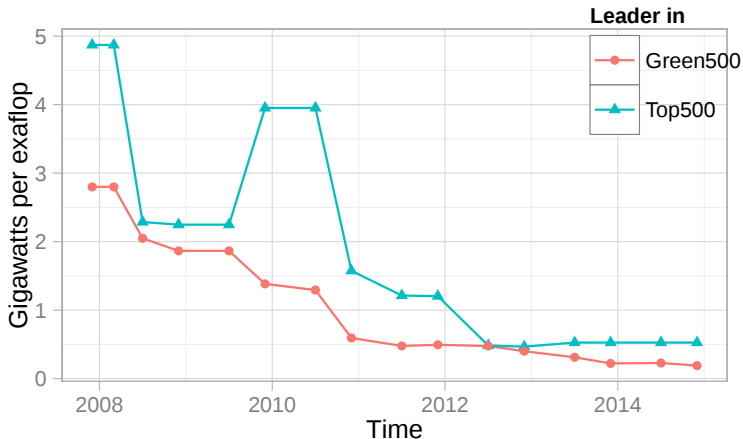
Conclusion

References

Top500 [4]



Extrapolating to Exascale [4, 5]



How can we improve energy-efficiency?

- Dynamic Voltage and Frequency Scaling [6]
- Dynamic Concurrency Throttling [7]

Technique

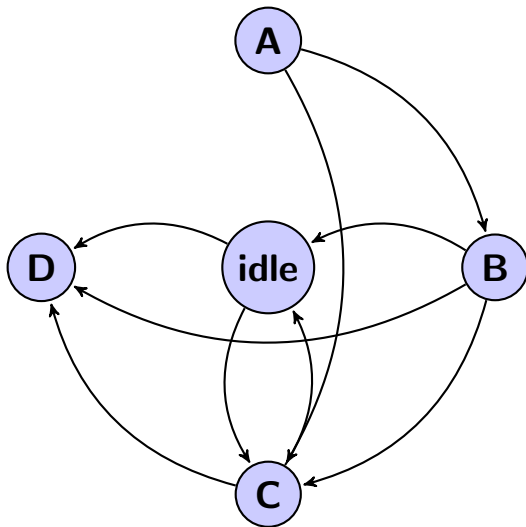
- User selects code regions to be optimized
- Build an execution profile
- Use profiling data to control clock speed

Technique

Cilk [8]

- Loops
- Tasks

Technique



Benchmarks

Benchmark Suite: PBBS [9]

Experimental Environment

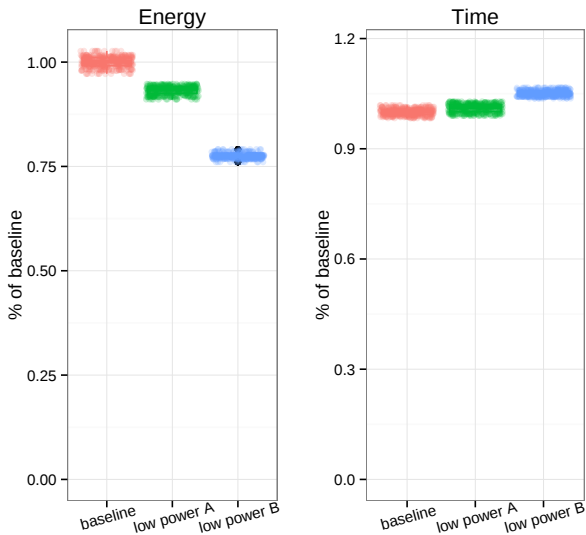
2× Intel(R) Xeon(R) CPU E5-2630

24 cores

32GB Memory

Results

Benchmark: Delaunay Refinement



Conclusion

- Improved energy-efficiency
- Low overhead
- Energy-efficiency gains limited to memory-bound problems

Future Work

- Reduce number of disadvantageous frequency changes
- Experiment with different runtimes
- Use other techniques like DCT

References I



N. Rajovic, L. Vilanova, C. Villavieja *et al.*, “The low power architecture approach towards exascale computing,” *Journal of Computational Science*, vol. 4, no. 6, pp. 439 – 443, 2013.



N. Kappiah, V. W. Freeh, and D. K. Lowenthal, “Just in time dynamic voltage scaling: Exploiting inter-node slack to save energy in mpi programs,” in *Proceedings of the 2005 ACM/IEEE Conference on Supercomputing*, ser. SC '05. Washington, DC, USA: IEEE Computer Society, 2005, pp. 33–.



K. Bergman, S. Borkar, D. Campbell *et al.*, “Exascale computing study: Technology challenges in achieving exascale systems peter kogge, editor and study lead,” 2008.



T. Top500, “500 supercomputer sites,” 2014.

References II

-  T. Scogland, B. Subramaniam, and W.-C. Feng, “The green500 list: Escapades to exascale,” *Comput. Sci.*, vol. 28, no. 2-3, pp. 109–117, May 2013.
-  J. Dongarra, P. Beckman, T. Moore *et al.*, “The international exascale software project roadmap,” *Int. J. High Perform. Comput. Appl.*, vol. 25, no. 1, pp. 3–60, Feb. 2011.
-  D. Li, B. de Supinski, M. Schulz, D. Nikolopoulos, and K. Cameron, “Strategies for energy-efficient resource management of hybrid programming models,” *Parallel and Distributed Systems, IEEE Transactions on*, vol. 24, no. 1, pp. 144–157, Jan 2013.
-  I. Corporation, “Intel cilk plus language specification,” Intel Corporation, Tech. Rep., 2011, revision: 1.1.

References III



J. Shun, G. E. Blelloch, J. T. Fineman *et al.*, “Brief announcement: the problem based benchmark suite.”

Questions?

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